

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

High-Voltage Transistor With Buried Conduction Layer

Application Number:

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Confirmation Number:

First Named Applicant:

Donald Disney

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Art Unit:

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Examiner:

Steven Ho Yin Loke

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(5146298 or 5155574 or 6184555 or 6168983 or 6207994 or 4618541 or 4665426 or 4890146 or 4922327 or 5386136 or 5894154 or 4626879 or 4754310 or 4764800 or 4811075 or 4926074 or 4963951 or 5010024 or 5025296 or 5040045 or 5068700 or 5237193 or 5258636 or 5270264 or 4939951 or 4967246 or 5313082 or 5637898 or 5654206 or 5656543 or 5659201 or 5663599 or 5716887 or 5324683 or 5349225 or 5359221 or 5411901 or 5521105 or 5550405 or 5670828

or 5679608 or 5943959 or 6010926 or

5998833).pn.

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Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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